

Wireless M-Bus High power N Mode RF Transceiver Module EN 13757-4:2013)

Product Description

The RC1701HP-MBUS is part of a compact surface-mounted Wireless M-Bus module family that measures only 12.7 x 25.4 x 3.3 mm. The module contains a communication controller with embedded wireless M-Bus protocol software supporting the new EN13757-4:2013 Mode N and is pre-certified for operation under the European radio regulations.

Applications

- Wireless M-Bus
- Automatic Meter Reading (AMR)
- Advanced Metering Infrastructure (AMI)
- Gas and Water meters
- · Electricity meters
- Heat meters, Heat cost allocators
- · Readers and concentrators
- Asset Tracking and Tracing



Note: The number of LGA pads differ from photo, see page 8 for details

Features

- Embedded Wireless M-Bus protocol supporting EN 13757-4:2013 mode N
- High power, long range (20 km Line-Of-Sight)
- Industry leading Wireless M-Bus protocol stack
- Completely shielded
- Pin compatible with the RC11XX (including –MBUS, –KNX and RC232 versions) and 2.4 GHz versions RC2500/2500HP from Radiocrafts
- 12.7 x 25.4 x 3.3 mm compact module for SMD mounting
- No external components except antenna
- 2.8 3.8 V supply voltage, ultra low power modes
- Conforms with EU R&TTE directive (EN 300 220, EN 301 489, EN 60950)

Quick Reference Data

Parameter	RC1701HP- MBUS	Unit
Frequency bands	169.4 – 169.475	MHz
Number of channels	10	
Data rate	2.4, 4.8, 19.2	kbps
Max output power	+ 27 dBm	dBm
Sensitivity, (2.4 / 4.8 / 19.2)	-119/-115/-107	dBm
Supply voltage	2.8 - 3.8	Volt
Current consumption, RX /IDLE	31,7	mA
Current consumption, TX (+27 dBm)	407	mA
Current consumption, SLEEP	Max 2.0	uA
Temperature range	-30 to +85	°C

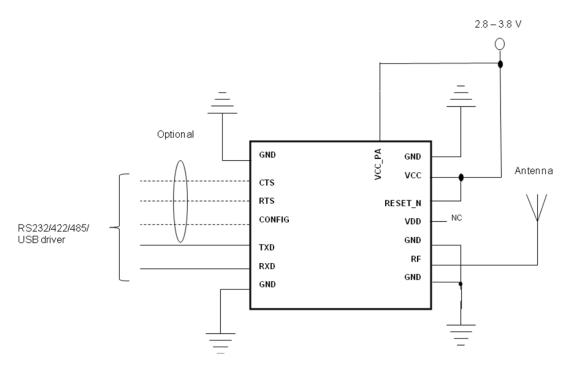
Part Name Overview

RC module	Max output power	VCC_PA
RC1701-MBUS4*	+15 dBm	N.C
RC1701VHP-MBUS4*	+30 dBm	VCC
RC1701HP-MBUS4	+27 dBm	VCC

^{*}Available on request



Typical application Circuit:



Note that the VCC_PA pin supply the internal power amplifier only while the rest of the internal block runs on VCC. They can be connected together or separated using individual supply. For the RC1701 variant the VCC_PA pin can be left open.

Wireless M-Bus Modem

The standard RC1701HP-MBUS module acts like a wireless M-Bus modem with a UART interface. The embedded protocol transmits and receives the wireless M-Bus data packets based on application messages from an external source (the meter or the concentrator). The module is configured through its UART interface using a simple command set. Configuration parameters are stored in non-volatile memory. The module can be set in Sleep mode with very low current consumption, and wake up on a UART command. See MBUS User Manual for details about the embedded wireless MBUS protocol from Radiocrafts.





RF Frequency, Output Power Levels and Data Rates

The following table shows the available RF channels and their corresponding frequencies, nominal output power levels and available data rates (Bold is default setting). The combination of frequency and data rate is specified in EN 13757-4:2013.

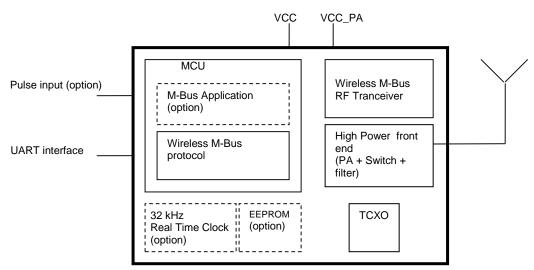
Model	RF channel	Output power	Data rate
RC1701HP- MBUS4	1: 169.406250 MHz (Channel 1a) 2: 169.418750 Mhz (Channel 1b) 3: 169.431250 MHz (Channel 2a) 4: 169.443750 MHz (Channel 2b) 5: 169.456250 MHz (Channel 3a) 6: 169.468750 MHz (Channel 3b) 7: 169.412500 MHz (Channel 1) 8: 169.437500 MHz (Channel 2) 9: 169.462500 MHz (Channel 3) 10: 169.437500 MHz (Channel 0)	5: +27 dBm 4: +24 dBm 3: +20 dBm 2: +17 dBm 1: +14 dBm	1: 2.4 kbps GFSK 2: 4.8 kbps GFSK 4: 19.2 kbps 4GFSK
RC1701VH P-MBUS4*	1: 169.406250 MHz (Channel 1a) 2: 169.418750 Mhz (Channel 1b) 3: 169.431250 MHz (Channel 2a) 4: 169.443750 MHz (Channel 2b) 5: 169.456250 MHz (Channel 3a) 6: 169.468750 MHz (Channel 3b) 7: 169.412500 MHz (Channel 1) 8: 169.437500 MHz (Channel 2) 9: 169.462500 MHz (Channel 3) 10: 169.437500 MHz (Channel 0)	5: +30 dBm 4: N.A 3: N.A 2: N.A 1: N.A	1: 2.4 kbps GFSK 2: 4.8 kbps GFSK 4: 19.2 kbps 4GFSK
RC1701- MBUS4*	1: 169.406250 MHz (Channel 1a) 2: 169.418750 Mhz (Channel 1b) 3: 169.431250 MHz (Channel 2a) 4: 169.443750 MHz (Channel 2b) 5: 169.456250 MHz (Channel 3a) 6: 169.468750 MHz (Channel 3b) 7: 169.412500 MHz (Channel 1) 8: 169.437500 MHz (Channel 2) 9: 169.462500 MHz (Channel 3) 10: 169.437500 MHz (Channel 0)	5: +15 dBm 4: +10 dBm 3: +6 dBm 2: 0 dBm 1: -40 dBm	1: 2.4 kbps GFSK 2: 4.8 kbps GFSK 4: 19.2 kbps 4GFSK

^{*}Available on request

For more details on changing the RF channel, output power or M-Bus mode, refer to the MBUS User Manual.



Block Diagram



Circuit Description

The module contains a communication controller with embedded Wireless M-Bus protocol software and a high performance RF transceiver. As an option the module can support a real time clock oscillator and EEPROM memory.

The communication controller handles the radio packet protocol, the UART interface and controls the RF transceiver. Data to be sent by the host is received at the RXD pin and buffered in the communication controller. The data packet is then assembled with preamble, start-of-frame delimited (SOF), manufacturer ID, unique address information and CRC check sums before it is transmitted on RF.

The RF transceiver modulates the data to be transmitted on RF frequency, and demodulates data that are received. Digital signal processing technology is used to enhance sensitivity and selectivity.

The high power front end amplifies the signal up to +27 dBm (+30 for RC1701VHP) and advanced filtering topology is included to suppress harmonics and spurs.

Received data are checked for correct CRC by the communication controller. If no CRC errors were detected, the data packet is sent to the host on the TXD line. The data format is configurable, and optionally an RSSI value (signal strength of received packet) can be added to the message.

The asynchronous UART interface consists of RXD and TXD. Optionally CTS or RTS can be used for hardware handshake flow control.

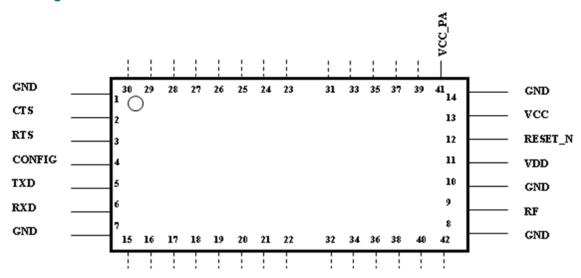
When a 00h value is sent as the first byte (replacing the Length byte), or the CONFIG pin is asserted, the module enters configuration mode and the communication controller interprets data received on the RXD pin as configuration commands. There are commands to change the radio channel, the output power, etc. Permanent changes of the configuration is also possible and are then stored in internal non-volatile memory (Flash).

The supply voltage is connected to the VCC and VCC_PA pin. The module contains an internal voltage regulator for the RF transceiver and can therefore operate over a wide supply voltage range.



The module can be set in Sleep mode by UART commands to reduce the power consumption to a minimum.

Pin Assignment



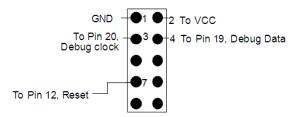
Pin Description

Pin	Pin name	Description		
no				
1	GND	System ground		
2	CTS/RXTX	UART Clear to Send / RXTX control (RS485)		
3	RTS/SLEEP	UART Request to Send		
4	CONFIG	Configuration Enable. Active low.		
5	TXD	UART TX Data		
6	RXD	UART RX Data		
7	GND	System ground		
8	GND	System ground		
9	RF	RF I/O connection to antenna		
10	GND	System ground		
11	VDD	Not Connected, Internal Regulator Output		
12	Reset	RESET_N. Active Low		
13	VCC	Supply voltage input. Internally regulated.		
14	GND	System ground		
41	VCC_PA	Supply voltage input for Power Amplifier stage. VCC_PA can be		
		connected together with VCC or separated using individual		
		supply.		
17-42	I/O	For future use and test status pin, Do not connect		



Programming Interface

For future firmware updates and possible custom variants it is recommended to include a 2x5 pins programming connector to the module programming pins. The connector should be a 2.54 mm pitch pin-row (same pitch in both directions), SMD or through-hole version, with the connections shown below.



Antenna Connection

The antenna should be connected to the RF pin. The RF pin is matched to 50 Ohm. If the antenna connector is placed away from the module at the motherboard, the track between the RF pin and the connector should be a 50 Ohm transmission line.

On a two layer board made of FR4 the width of a microstrip transmission line should be 1.8 times the thickness of the board, assuming a dielectric constant of 4.8. The line should be run at the top of the board, and the bottom side should be a ground plane.

Example: For a 1.6 mm thick FR4 board, the width of the trace on the top side should be $1.8 \times 1.6 \text{ mm} = 2.88 \text{ mm}$.

The simplest antenna to use is the quarter wave whip antenna. A quarter wave whip antenna above a ground plane yields 37 Ohm impedance and a matching circuit for 50 Ohm are usually not required.

A PCB antenna can be made as a copper track where the ground plane is removed on the back side. The rest of the PCB board should have a ground plane as large as possible, preferably as large as the antenna itself, to make it act as a counterweight to the antenna. If the track is shorter than a quarter of a wavelength, the antenna should be matched to 50 ohms.

The length of a quarter wave antenna at 169.4 MHz is typ 42 cm long. Contact Radiocrafts for support on antenna design.

Regulatory Compliance Information

The use of RF frequencies and maximum allowed RF power is limited by national regulations. The RC1701HP-MBUS has been designed to comply with the R&TTE directive 1999/5/EC.

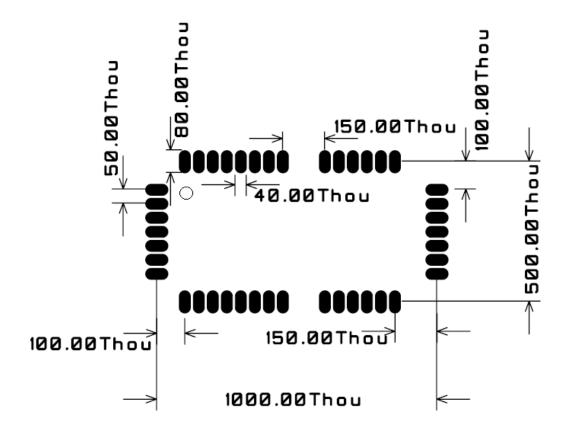
According to R&TTE directives, it is the responsibility of Radiocrafts' customers (i.e. RC1701HP-MBUS end user) to check that the host product (i.e. final product) is compliant with R&TTE essential requirements. The use of a CE marked radio module can avoid recertification of the final product, provided that the end user respects the recommendations given by Radiocrafts. A Declaration of Conformity is available from Radiocrafts on request.

The relevant regulations are subject to change. Radiocrafts AS do not take responsibility for the validity and accuracy of the understanding of the regulations referred above. Radiocrafts only guarantee that this product meets the specifications in this document. Radiocrafts is exempt from any responsibilities related to regulatory compliance.



PCB Layout Recommendations

The recommended layout pads for the module are shown in the figure below. All dimensions are in thousands of an inch (mil). The circle in upper left corner is an orientation mark only, and should not be a part of the copper pattern.



A PCB with two or more layers and with a solid ground plane in one of the inner- or bottom layer(s) is recommended. All GND-pins of the module shall be connected to this ground plane with vias with shortest possible routing, one via per GND-pin.

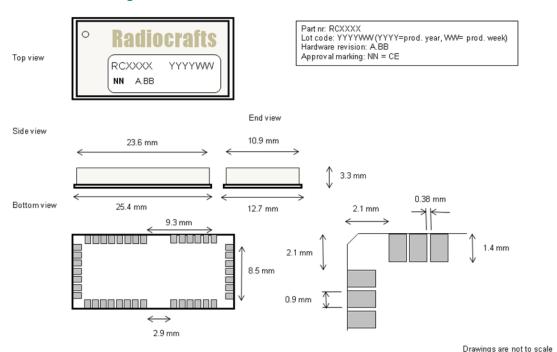
On the back side of the module there are several vias and pads. These vias and pads shall not be connected, and the area underneath the module should be covered with solder resist. If any routing or vias is required under the module, the routing and vias must be covered with solder resist to prevent short circuiting to the module bottom side vias and pads. It is recommended that vias are tented.

Reserved pins should be soldered to the pads but the pads must be left floating.

Note that Radiocrafts technical support team is available for schematic and layout review of your design.



Mechanical Drawing



Mechanical Dimensions

The module size is 12.7 x 25.4 x 3.3 mm

Carrier Tape and Reel Specification

Carrier tape and reel is in accordance with EIA Specification 481.

Tape width	Component pitch	· ·		Units per reel
44 mm	16 mm	4 mm	13"	Max 1000

Soldering Profile Recommendation

JEDEC standard IEC/JEDEC J-STD-020B (page 11 and 12), Pb-Free Assembly is recommended.

The standard requires that the heat dissipated in the "surroundings" on the PCB is taken into account. The peak temperature should be adjusted so that it is within the window specified in the standard for the actual motherboard.

Aperture for paste stencil is normally areal-reduced by 20-35%, please consult your production facility for best experience aperture reduction.



Absolute Maximum Ratings

Parameter	Min	Max	Unit
Supply voltage, VCC	-0.3	3.8	V
Supply voltage, VCC_PA	-0.3	3.8	V
Voltage on any pin	-0.3	VCC+0.3V	V
Input RF level		10	dBm
Storage temperature	-50	150	°C
Operating temperature	-40	85	°C



Caution! ESD sensitive device. Precaution should be used when handling the device in order to prevent permanent damage.

Under no circumstances the absolute maximum ratings given above should be violated. Stress exceeding one or more of the limiting values may cause permanent damage to the device.

Electrical Specifications

T=25°C, VCC = 3.3V, VCC_PA=3.3V if nothing else stated.

Number of 12.5 kHz channels Sumber of 25 kHz channels Sumber of 25 kHz channels Sumber of 25 kHz channels Sumber of 50 kHz channels	Parameter	Min	Тур.	Max	Unit	Condition / Note
Number of 12.5 kHz channels Sumber of 25 kHz channels Sumber of 25 kHz channels Sumber of 50 kHz channels	Operating frequency					
Number of 25 kHz channels 3 25 kHz channels Number of 50 kHz channels 1 25 kHz channels Input/output impedance 50 Ohm Data rate 2.4 kbit/s 2GFSK 4.8 19.2 kHz 2GFSK 4GFSK 2GFSK 4GFSK Frequency tolerance 12.5 kHz channels +/-1.5 kHz Including 10 years of aging. Frequency stability aging 1 ppm/year Starting after 10 years Frequency stability aging 27 27.5 dBm Typical values are for default settings Frequency stability aging 27 27.5 dBm dBm dFault settings Frequency stability aging 1 ppm/year Typical values are for default settings Typical values are for default settings Frequency stability aging 15 16 FSK deviation KHz KHz Typical values are for default settings Frequency stability aging 15 16 FSK deviation KHz KHz Typical values are for default settings 192 kbps <td></td> <td>169.4</td> <td></td> <td>169.475</td> <td>MHz</td> <td></td>		169.4		169.475	MHz	
Number of 25 kHz channels 3 25 kHz channels Number of 50 kHz channels 1 25 kHz channels Input/output impedance 50 Ohm Data rate 2.4 kbit/s 2GFSK 4.8 19.2 kHz 2GFSK 4GFSK 2GFSK 4GFSK Frequency tolerance 12.5 kHz channels +/-1.5 kHz Including 10 years of aging. Frequency stability aging 1 ppm/year Starting after 10 years Frequency stability aging 27 27.5 dBm Typical values are for default settings Frequency stability aging 27 27.5 dBm dBm dFault settings Frequency stability aging 1 ppm/year Typical values are for default settings Typical values are for default settings Frequency stability aging 15 16 FSK deviation KHz KHz Typical values are for default settings Frequency stability aging 15 16 FSK deviation KHz KHz Typical values are for default settings 192 kbps <td>November of 40 5 bills above als</td> <td></td> <td></td> <td></td> <td></td> <td></td>	November of 40 5 bills above als					
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Data rate	Input/output impedance				Ohm	
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2.4	Data rata					
A.8 19.2	Data rate		2.4		khit/s	2GESK
19.2 4GFSK 4GFSK Frequency tolerance 12.5 kHz channels +/-1.5					Kolyo	
Frequency tolerance 12.5 kHz channels 50 kHz channels 12.5 kHz channels 50 kHz channels 1						
12.5 kHz channels						
Frequency stability aging						
Frequency stability aging Transmit power RC1701HP-MBUS4 RC1701VHP-MBUS4 RC1701-MBUS4 RC1701-MBUS4 RC1701-MBUS4 RC1701-MBUS4 RC1701-MBUS4 RC1701-MBUS4 RSH deviation 2.4 kbps 4/- 2.4 4.8 kbps 4/- 7.2 / 2.4 4.8 kbps 4/- 7.2 / 2.4 4.9 kdjacent channel power: 12.5 kHz channels 25 and 50 kHz channels Spurious emission, TX < 1 GHz > 1 GHz > 1 GHz > 1 GHz Sestricted bands Sensitivity: 2.4 kbps 4-118 -119 -118 -119 -18 kbps -114 -115 -106 -107 Starting after 10 years Typical values are for default settings Refault settings AdBm FX AdBm FX FX FX FX FX FX FX FX FX F					kHz	
Transmit power RC1701HP-MBUS4 RC1701VHP-MBUS4 RC1701-MBUS4 RC1701-MBUS	50 kHz channels			+/-4.25		aging.
Transmit power RC1701HP-MBUS4 RC1701VHP-MBUS4 RC1701-MBUS4 RC1701-MBUS	Frequency stability aging			1	nnm/vear	Starting after 10 years
RC1701HP-MBUS4 RC1701VHP-MBUS4 RC1701-MBUS4 27 30 30.5 16 27.5 30.5 16 dBm default settings FSK deviation 2.4 kbps 4.8 kbps 4.6 kbps 4.8 kbps 4.8 kdps 4.8				-		
RC1701VHP-MBUS4 30 30.5 16						Typical values are for
RC1701-MBUS4 15 16 FSK deviation 2.4 kbps +/- 2.4 kHz 4.8 kbps +/- 2.4 kHz 19.2 kbps +/- 7.2 / 2.4 Adjacent channel power: 2.5 kHz channels <-20				-	dBm	default settings
FSK deviation 2.4 kbps 4.8 kbps 19.2 kbps 4/- 2.4 4/- 2.4 4/- 7.2 / 2.4 Adjacent channel power: 12.5 kHz channels 25 and 50 kHz channels Spurious emission, TX < 1 GHz > 1 GHz > 1 GHz Restricted bands -36 -30 Restricted bands: 47 MHz – 74 MHz 47 MHz – 74 MHz 87.5 MHz – 118 MHz 174 MHz – 230 MHz 470 MHz – 862 MHz Sensitivity: 2.4 kbps -118 -119 -18 -19 -18 -19 -106 -107 Measured at 1% BER / 80% PER of 20 byte packets.						
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4.8 kbps						
19.2 kbps -106 -107					dBm	
						packets.
Adjacent channel rejection 64 dB	19.2 KDPS	-106	-107			
, agason sharmor rejection	Adjacent channel rejection		64		dB	
Alternate channel selectivity 66 dB	Alternate channel selectivity		66		dB	
Image channel rejection 66 dB	Image channel rejection		66		dB	



Blocking / Interferer rejection / desensitization +/- 1 MHz +/- 2 MHz +/- 10 MHz	30 35 60	82 83 89		dB	Wanted signal 3 dB above sensitivity level, CW interferer. Minimum numbers corresponds to class 2 receiver requirements in EN300220.
Saturation		+10		dBm	
Input IP3		-14		dBm	
Spurious emission, RX			-57	dBm	
Supply voltage, VCC VCC_PA	2.8 2.5	3.3 3.3	3.8 3.8	V	
Current consumption, RX/IDLE VCC VCC_PA		31 0.3	32	mA uA	Apply over entire supply voltage range
RC1701HP Current, TX: RF_POWER=5, +27 dBm RF_POWER=4, +24 dBm RF_POWER=3, +20 dBm RF_POWER=2, +17 dBm RF_POWER=1, +14 dBm		VCC_PA+VCC 407 268 173 132 103		mA	Apply over entire supply voltage range
RC1701VHP Current, TX: RF_POWER=5, +30 dBm		VCC_PA+VCC 651 mA			
RC1701 Current, TX: RF_POWER=5, +15 dBm		VCC_PA+VCC 57 mA			
Current consumption, SLEEP VCC VCC_PA		0.60 0.02	2.0 1.0	uA uA	
Digital I/O Input logic level, low Input logic level, high Output logic level, low (1µA) Output logic level, high(-1µA)	70 % 0 TBD		30 % TBD VCC	V	Of VCC Of VCC
RESET pin Input logic level, low Input logic level, high	70 %		30 %	V	Minimum 250 ns pulse width
UART Baud Rate tolerance		+/- 2		%	UART receiver and transmitter
Configuration memory write cycles	1000				The guaranteed number of write cycles using the 'M' command is limited



Document Revision History

Document Revision	Changes
1.0	First release

Product Status and Definitions

Current Status	Data Sheet Identification	Product Status	Definition
	Advance Information	Planned or under development	This data sheet contains the design specifications for product development. Specifications may change in any manner without notice.
X	Preliminary	Engineering Samples and First Production	This data sheet contains preliminary data, and supplementary data will be published at a later date. Radiocrafts reserves the right to make changes at any time without notice in order to improve design and supply the best possible product.
	No Identification Noted	Full Production	This data sheet contains final specifications. Radiocrafts reserves the right to make changes at any time without notice in order to improve design and supply the best possible product.
	Obsolete	Not in Production	This data sheet contains specifications on a product that has been discontinued by Radiocrafts. The data sheet is printed for reference information only.



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As far as possible, major changes of product specifications and functionality, will be stated in product specific Errata Notes published at the Radiocrafts website. Customers are encouraged to check regularly for the most recent updates on products and support tools.

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RC232™ is a trademark of Radiocrafts AS. The RC232™ Embedded RF Protocol is used in a range of products from Radiocrafts. The protocol handles host communication, data buffering, error check, addressing and broadcasting. It supports point-to-point, point-to-multipoint and peer-to-peer network topologies.

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